

FIG. 1A
(PRIOR ART)

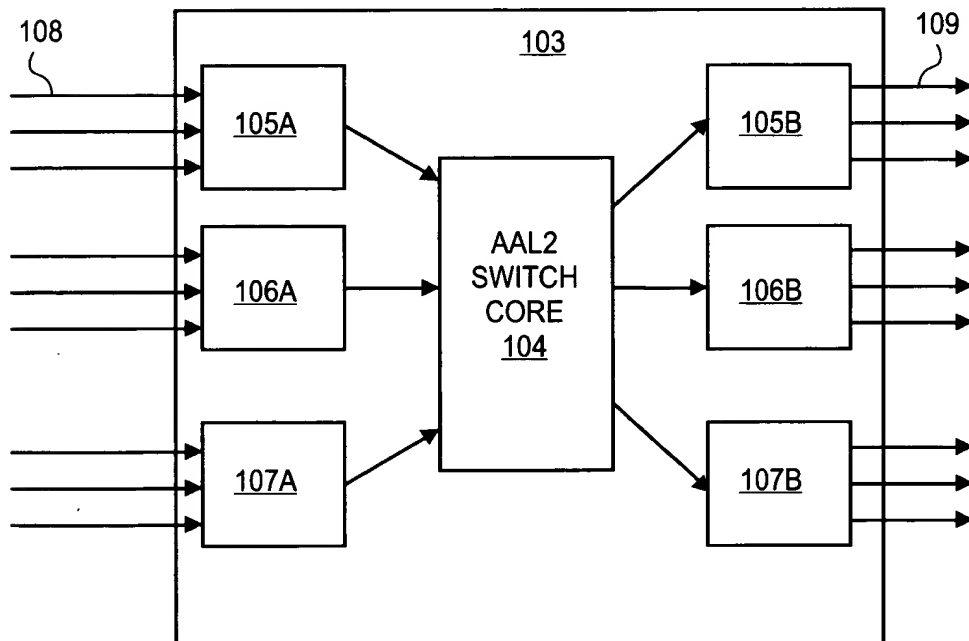


FIG. 1B
(PRIOR ART)

200

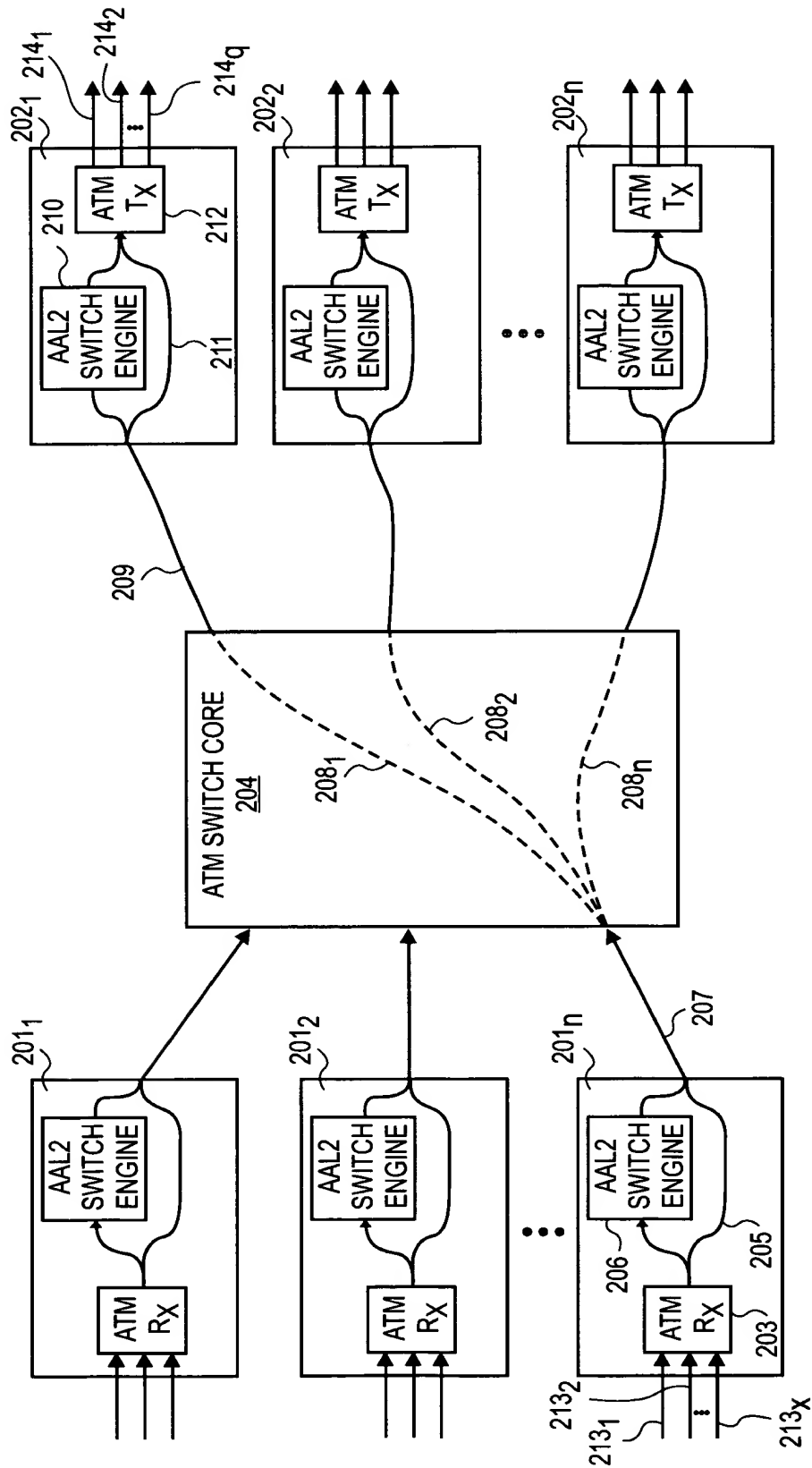


FIG. 2

IN			OUT	
PORT#	VPI/VC	CID	EID	P#
A ₁	A ₂	A ₃	A ₄	A ₅
B ₁	B ₂	B ₃	B ₄	B ₅
C ₁	C ₂	C ₃	C ₄	C ₅
q ₁	q ₂	q ₃	q ₄	q ₅

300

FIG. 3

IN	OUT		
EID	CID	VPI/VCI	PORT#
X ₄	X ₆	X ₇	X ₈
Y ₄	Y ₆	Y ₇	Y ₈
Z ₄	Z ₆	Z ₇	Z ₈
⋮			
m ₄	m ₆	m ₇	m ₈

FIG. 4

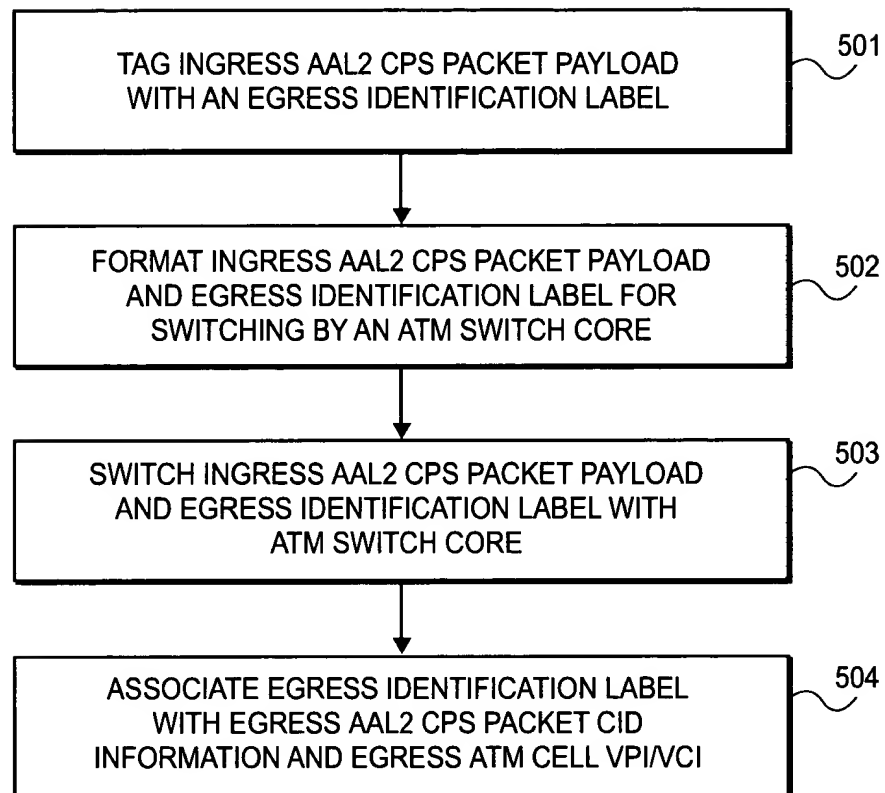


FIG. 5